



Prith Banerjee

Senior Vice President,
Simulation & Analysis
Incubation Group

Prith Banerjee is Senior Vice President of Simulation & Analysis Incubation at Synopsys. In this role, he brings the voice of simulation and analysis (S&A) sales and customers to the Synopsys Executive Leadership Team, reporting directly to the CEO. Prith plays a critical role in providing background, knowledge, and credibility for the S&A solution as Synopsys builds relationships with Ansys customers that are new to the company. He also supports the strategic evolution of the company's simulation portfolio and incubation efforts.

Prior to joining Synopsys, Prith served as Chief Technology Officer at Ansys, where he was responsible for guiding the company's long-term technology vision. He has held multiple senior leadership roles across technology and industrial organizations, including Executive Vice President and Chief Technology Officer at Schneider Electric, Chief Technology Officer and Executive Vice President at ABB in Zurich, and Managing Director of Global Technology R&D at Accenture. He also served as Senior Vice President of Research and Director of HP Labs at Hewlett-Packard, where he oversaw global innovation efforts.

Earlier in his career, Prith was a leader in academia, serving as Dean of the College of Engineering at the University of Illinois at Chicago and as Chairman of the Electrical and Computer Engineering Department at Northwestern University. He began his academic career as a professor at the University of Illinois at Urbana-Champaign.

A serial entrepreneur, Prith founded AccelChip, an EDA software company acquired by Xilinx, and BINACHIP, where he served as Founder, Chairman, and Chief Scientist. His research interests span electronic design automation and parallel computing, and he is the author of over 350 technical papers and has supervised 37 Ph.D. students.

Prith has been widely recognized for his contributions to technology and innovation. He was named one of Fast Company's Top 100 Business Leaders in 2009 and is a Fellow of AAAS, ACM, IEEE and National Academy of Inventors. He is also a recipient of the ASEE Terman Award and the NSF Presidential Young Investigator Award.

He earned his B.Tech. in Electronics Engineering from the Indian Institute of Technology, Kharagpur, where he was awarded the President's Gold Medal. He holds an M.S. and Ph.D. in Electrical Engineering from the University of Illinois at Urbana-Champaign.