

Charlie Matar

Senior Vice President, IP
and Design Solutions

Charlie Matar leads the IP Group (IPG) at Synopsys. In this capacity, he leads the organization responsible for the development of Synopsys' Intellectual Property (IP) portfolio including interface IP, foundation IP and Processors. In addition, Charlie leads the System Solutions Group (SSG) delivering full System on Chips (SoC) design solutions and services.

He is a technology executive with a strong background in building and managing business units and global engineering teams. He has a proven track record of overseeing multiple projects, guiding them from product definition, SoC and IP development to production ramp using advanced process technologies in market segments like high-performance computing, mobile, automotive, and IoT/wearable devices.

Prior to joining Synopsys, he held executive roles at TSMC North America, Intel, AMD, and Qualcomm. Charlie has a BSEE in semiconductor physics from the University of Texas, Austin, and an MSEE in microelectronics from Southern Methodist University.